

S/N 10/728,655

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Michael J. O'Phelan et al.	Examiner: Ha T. Nguyen
Serial No.:	10/728,655	Group Art Unit: 2812
Filed:	December 5, 2003	Docket: 279.168US2
Title:	FLAT CAPACITOR HAVING STAKED FOILS AND EDGE-CONNECTED CONNECTION MEMBERS	

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11/325,931	January 5, 2006	279.339US4	METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT CAPACITOR.
11/277,813	March 29, 2006	279.234US5	FLAT CAPACITOR FOR AN IMPLANTABLE MEDICAL DEVICE

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

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By Applicants' Representatives,

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6/29/06

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29th day of June, 2006.

Name

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Signature

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